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**Test Methods for
Characterization of
Printed Board Assembly
Pad Cratering**

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A standard developed by IPC

Association Connecting Electronics Industries



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Developed by the SMT Attachment Reliability Test Methods Task Group (6-10d) of the Product Reliability Committee (6-10) of IPC

Users of this publication are encouraged to participate in the development of future revisions.

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Acknowledgment

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Test Methods for Characterization of Printed Board Assembly Pad Cratering

1 SCOPE

This document provides test methods to evaluate the susceptibility of printed board assembly (PBA) materials and designs to cohesive dielectric failure underneath surface mount technology (SMT) attach pads. The test methods can be used to rank or order and compare different printed board materials and design parameters, but do not define acceptance criteria.

1.1 Performance Classification This test method guideline recognizes that surface mount assemblies (SMAs) will be subject to variations in performance requirements based on end use. While performance classes are defined in IPC-6011, these performance classifications are not specific as to the required reliability. As of the publication of this standard, the acceptance criteria needs to be established as agreed between user and supplier (AABUS).

1.2 Definition of Terms The definition of all terms used herein **shall** be as specified in IPC-T-50, except as otherwise specified in 1.2.1 through 1.2.4.

1.2.1 BGA Ball Grid Array package.

1.2.2 Component Packaged semiconductor device.

1.2.3 Solder Joint/Ball The solder interconnection between a component and PBA.

1.2.4 Pad Cratering The formation of a cohesive (or adhesive) dielectric crack or fracture underneath the pad of a surface mount component, most commonly BGA packages.

1.3 Interpretation “**Shall**” is used throughout this specification when a requirement is intended to express a provision that is mandatory; deviation may be considered if sufficient data is supplied to justify the exception.

The words “should” and “may” are used whenever it is necessary to express non-mandatory provisions. “Will” is used to express a declaration of purpose. To assist the reader, the word “**shall**” is presented in bold characters.

2 APPLICABLE DOCUMENTS

The following documents are applicable and constitute a part of this specification to the extent specified herein. Sub-sequent issues of, or amendments to, these documents will be considered a part of this specification. Documents are grouped under categories such as IPC, Joint Industry Standard, and others depending on the source.

2.1 IPC¹

IPC-T-50 Terms and Definitions for Interconnecting and Packaging Electronic Circuits

IPC-6011 Qualification and Performance Specification for Printed Boards

2.2 Joint Electron Device Engineering Council²

JESD22-B117A BGA Ball Shear

JESD22-B115 Solder Ball Pull

1. www.ipc.org

2. www.jedec.org