

IEEE Standard for Software-Hardware Interface for Multi-Many-Core

IEEE Computer Society

Developed by the
Design Automation Standards Committee

IEEE Std 2801™-2019

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Developed by the

Design Automation Standards Committee
of the
IEEE Computer Society

Approved 7 November 2019

IEEE SA Standards Board

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Abstract: This standard is intended primarily for tool developers and hardware developers who would use Software Hardware Interface for Multi-Many-core (SHIM) to exchange hardware description for software tools. It also attempts to provide software developers with insights into what hardware information is described in SHIM to foster understanding of the intention and the extent of SHIM.

Keywords: IEEE 2804, many-core, multicore, SHIM, software hardware interface, software tools

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Introduction

This introduction is not part of IEEE Std 2804-2019, IEEE Standard for Software-Hardware Interface for Multi-Many-Core.

This document is intended primarily for tool developers and hardware developers who would use SHIM to exchange hardware description for software tools. It also attempts to provide software developers with insights into what hardware information is described in SHIM to foster understanding of the intention and the extent of SHIM.

This document begins with the introduction to SHIM, providing the background, the overall concept and model. It is followed by a clause detailing the concept of SHIM, such as the purpose, scope, design, interface, limitation, providing the basic idea why SHIM is as specified in this document and also trying to explain the basic principles for future extension of the specification. A clause describing the interface follows, which is a description of SHIM XML schema and APIs that are mostly derived directly from the schema via XML data binding technique. A clause providing some of the detailed use cases follows, allowing the reader to gain insights into how SHIM can be used in action. Finally, this document ends with various annexes that provide further detailed information.

Contents

1. Overview	10
1.1 Scope	10
1.2 Purpose	10
1.3 Word usage	10
1.4 General introduction	11
2. Normative references	18
3. Definitions	8
4. SHIM concepts	18
4.1 Topology—ComponentSet	18
4.2 Inter-core communication—CommunicationSet	19
4.3 Frequency and voltage—FrequencyVoltageSet	20
4.4 Communication network utilization and contention—ContentionGroupSet	21
4.5 Performance	22
4.6 Power—PowerConfiguration	26
4.7 Vendor extensions	26
4.8 Configuration	27
5. Roadmap	29
5.1 General	29
5.2 Further componentization of SHIM XML	30
5.3 Hardware-related software properties	30
5.4 Schema refinement for smaller XML	30
6. SHIM interface	31
6.1 shim20.xsd	31
6.2 Conventions	40
6.3 Enumeration	40
6.4 Shim	42
6.5 SystemConfiguration	42
6.6 ComponentSet	43
6.7 FrequencyVoltageSet	53
6.8 AddressSpaceSet	56
6.9 CommunicationSet	59
6.10 ContentionGroupSet	63
6.11 PowerConfiguration	65
6.12 Vendor Extension	66
7. Use cases	68
7.1 Performance estimation: Auto-parallelizing compiler	68
7.2 Tool configuration—RTOS configuration tool	70
7.3 Hardware modeling	71
8. SHIM XML authoring rules and guidelines	71
8.1 File name [rule]	72
8.2 The naming of various objects [rule]	73
8.3 Level of detail and precision [guideline]	73
9. Common Configuration File (CCF)	73

9.1 Concept.....	73
9.2 Interface.....	75
9.3 Examples	79
10. FAQ.....	80

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IEEE Standard for Software-Hardware Interface for Multi-Many-Core

1. Overview

1.1 Scope

The scope of this standard includes performance estimation accuracy for complex processors like Very Long Instruction Word (VLIW) core and complex contention scenarios, description of caches to include uncached memory regions and caches for subsets of memories, properties for coarse power consumption estimation, and reusability by separating eXtensible Markup Language (XML) files for processor description and other memory/communication-related information.

1.2 Purpose

The Software-Hardware Interface for Multi-Many-core (SHIM) defines an architecture description standard from the software design perspective—this provides a common interface that abstracts the hardware properties that are critical to enable multicore tools.

1.3 Word usage

The word *shall* indicates mandatory requirements strictly to be followed in order to conform to the standard and from which no deviation is permitted (shall equals is required to).^{1, 2}

The word *should* indicates that among several possibilities one is recommended as particularly suitable, without mentioning or excluding others; or that a certain course of action is preferred but not necessarily required (should equals is recommended that).

The word *may* is used to indicate a course of action permissible within the limits of the standard (may equals is permitted to).

The word *can* is used for statements of possibility and capability, whether material, physical, or causal (can equals is able to).

¹The use of the word *must* is deprecated and cannot be used when stating mandatory requirements, *must* is used only to describe unavoidable situations.

²The use of *will* is deprecated and cannot be used when stating mandatory requirements, *will* is only used in statements of fact.